



Software Selectable Pins

Interface	Signal	Pin
EMAC	EMAC_WBC_out	
	EMAC_MDIO_in	Any GPIO
	EMAC_CRSD_out	
	EMAC_COL_out	
I2C	I2CXT0_SCL_in	
	I2CXT0_SDA_in	
	I2CXT1_SCL_out	
	I2CXT1_SDA_out	
General Purpose SPI	HSPICLK_in_out	
	HSPICLK_in_out	
	HSPICLK_in_out	
	HSPICLK_in_out	
	HSPICLK_in_out	
	HSPICLK_in_out	

For more information refer to esp_wroom_32_datasheet_en.pdf.

Bootstrapping Pins Settings

Pin	Default	Voltage of Internal LDO(VDD_S0I6)	I.BV
GPIO12/MTDI	Pull-Down	0	1
Bootstrapping Mode			
Pin	Default	SPI Flash Boot	Download Boot
GPIO0	Pull-Up	0	0
GPIO4	Pull-Down	Don't-care	0
Debugging Log on UOTXD During Booting			
Pin	Default	UOTXD Toggling	UOTXD Silent
GPIO15/MTDO	Pull-Up	0	0
Timing of S0I6 Slave			
Pin	Default	Falling-edgeOutput	Rising-edgeOutput
GPIO15/MTDO	Pull-Up	0	1
GPIO9/PHY_PWR	Pull-Up	0	1

Internal Bootstrapping Resistors

MTDI/GPIO12:	Pull-Down
GPIO0:	Pull-Up
GPIO2:	Pull-Down
MTDO/GPIO15:	Pull-Up
GPIO9/PHY_PWR:	Pull-Up

