

WHITECAT
ESP32 N1 REV 1A

WHITECATBOARD.ORG

AE2

P2
GND P13 P12 P11 P10 P9 P8 P7 P6 P5 P4 P3 P2 P1 P0 P10 P11 P12 P13 GND

C13 C12 C11 C10 R6 C9 C7 Y1 C8 C25 D1 R7
C14 C15 L3 U3

C17 C18 C19 C20 C21 C22 Y2 C23

C16 R2 R3 C5 R4 R5 C6

C1 U1 GND BAT RX TX RST PRG 3V3 GND REN OUT GND P01 P02 P03 P04 P05
L1 C24 C3 U2 L2 C4

RST
PRG

P1



UFL2

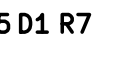
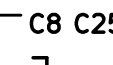
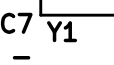
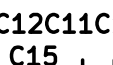
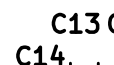
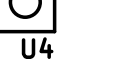
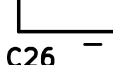
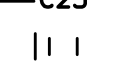
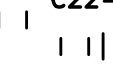
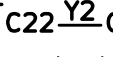
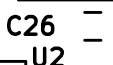
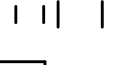
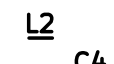
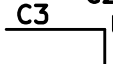
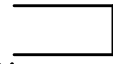
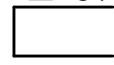
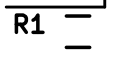
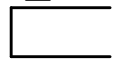
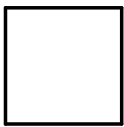
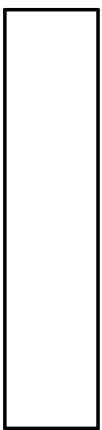
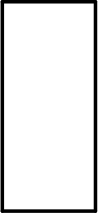
C27

U5



UFL1

AE1



P2

GND P13 P12 P11 P10 P9 P8 P7 P6 P5 P4 P3 P2 P1 P0 P10 P11 P12 P13 GND

C13 C12 C11 C10 R6 C9 C7 Y1 C8 C25 D1 R7
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RST
PRG

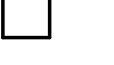
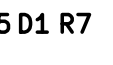
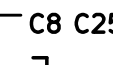
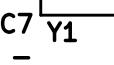
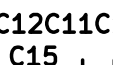
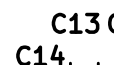
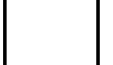
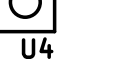
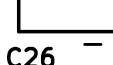
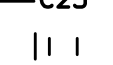
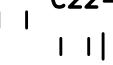
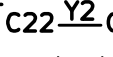
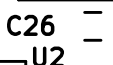
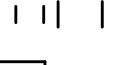
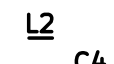
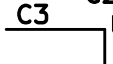
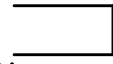
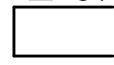
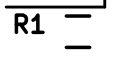
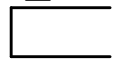
P1



UFL2

C27

U5



P2

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C13 C12 C11 C10 R6 C9 C7 Y1 C8 C25 D1 R7
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L1 C24 C3 U2 L2 C4

RST
PRG

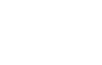
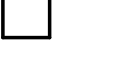
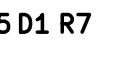
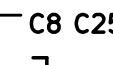
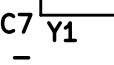
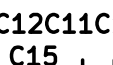
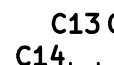
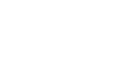
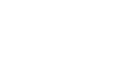
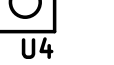
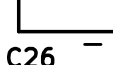
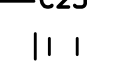
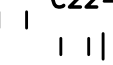
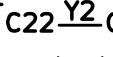
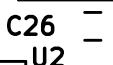
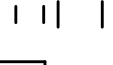
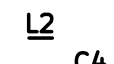
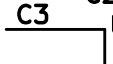
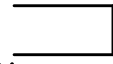
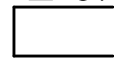
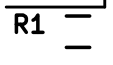
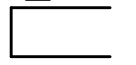
P1



UFL2

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U5



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